



PATENT ABSTRACTS OF JAPAN

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(21) Application number: **2002062039**(22) Date of filing: **07.03.2002**(71) Applicant: **NEC CORP**(72) Inventor: **NAWATA HIDE**(54) **VARIABLE COMMUNICATION SYSTEM**

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a variable communication system which unnecessitates a buffer memory required in changing a communication rate, while switching a communication mode in response to a communication amount per unit time.

SOLUTION: A communication amount monitor circuit 124 monitors a transmission buffer 123 for inputting communication data 122 so as to discriminate the largeness, reads information from the buffer 123 by chip clock when the information amount is comparatively large, modulates the information by a modulator 139 as communication data 137, and transmits them. When the information amount is comparatively small, the circuit 124 reads information from the transmission buffer 123 by a clock which is obtained by frequency dividing the chip clock, obtains exclusive ORs together

with a diffusion code which is generated through the use of the chip clock, modulates it by the modulator 139, and transmits it as communication data 137. Since the frequency is not changed in communication data 137, re-synchronization is not required in the case of changing the information amount at the side of a receiving device. Thus, the buffer area is unnecessary.

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